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CLAIMS

1. A semiconductor memory card which performs contactless communication with a reader/writer, the semiconductor memory card comprising:

a direction obtainment unit operable to obtain a first access direction type in which the semiconductor memory card accesses the reader/writer;

a condition management unit operable to previously hold and manage an access condition including two or more of second access directions types;

a condition judgment unit operable to compare two or more of the first access direction types obtained by the direction obtainment unit with two or more of the second access direction types included in the access condition held in the condition management unit, and to judge whether or not the first and second access direction types match each other; and

an execution unit operable to execute a predetermined application program when the condition judgment unit judges that the first and the second access direction types match each other.

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 A semiconductor memory card according to Claim 1, wherein the access condition includes a time series pattern of the second access direction types, and

the condition judgment unit is operable to compare the first and second access direction types according to the time series pattern.

- A semiconductor memory card according to Claim 1,
 wherein one of the access direction types is an access side
 type of the semiconductor memory card.
 - 4. A semiconductor memory card according to Claim 1,

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wherein one of the access direction types is a horizontal access direction type on a same side of the semiconductor memory card.

- 5. A semiconductor memory card according to Claim 1, wherein the direction obtainment unit is operable to obtain the first access direction types upon a lapse of a predetermined time from a time point at which electric power is generated by electromagnetic waves supplied by the reader/writer exceeds a predetermined voltage.
 - 6. A semiconductor memory card according to Claim 1, wherein the access condition includes a number of comparisons of the first and second access direction types, and the condition judgment unit is operable to compare the first and second access direction types as many as the number of comparisons.

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 A semiconductor memory card according to Claim 6, wherein the access condition includes a time limit for the semiconductor memory card to access the reader/writer,

the condition judgment unit is operable to judge whether or not an elapsed time from a first access to a completion of comparisons as many as the number of comparisons is within the time limit, and

the execution unit is operable to execute the predetermined application program when the condition judgment unit judges that the elapsed time is within the time limit.

- 8. A semiconductor memory card according to Claim 1, further comprising
 - a condition obtainment unit operable to obtain the access

condition from an external device,

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wherein the condition management unit is operable to store and manage the access condition.

9. A server which is connected to a reader/writer via a network, the reader/writer performing contactless communication with a semiconductor memory card, the server comprising:

a direction obtainment unit operable to obtain a first access direction type in which the semiconductor memory card accesses the reader/writer;

a condition management unit operable to previously hold and manage an access condition including two or more of second access direction types;

a condition judgment unit operable to compare two or more of the first access direction types obtained by the direction obtainment unit with two or more of the second access direction types included in the access condition held in the condition management unit, and to judge whether or not the first and second access direction types match each other, and

a notification unit operable to notify the judgment result of the semiconductor memory card.

10. A method of providing a service by a system including a semiconductor memory card which performs contactless communication with a reader/writer, and a server connected to the reader/writer via a network, the method comprising:

obtaining a first access direction type in which the semiconductor memory card accesses the reader/writer;

comparing two or more of the first access direction types obtained in the obtaining with two or more of second access direction types previously held in a storing unit, and judging whether or not the first and second access direction types match

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each other; and

executing a predetermined application program when it is judged, in the judging, that the first and second access direction types match each other.

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11. A program for performing contactless communication with a reader/writer, the program causing a computer to execute:

obtaining a first access direction type in which the semiconductor memory card accesses the reader/writer;

comparing two or more of the first access direction types obtained in the obtaining with two or more of second access direction types previously held in a storing unit, and judging whether or not the first and second access direction types match each other; and

executing a predetermined application program when it is judged, in the juding, that the first and second direction types match each other.

12. A computer readable recording medium in which a program for performing contactless communication with a reader/writer is recorded, the program comprising:

obtaining a first access direction type in which the semiconductor memory card accesses the reader/writer;

comparing two or more of the first access direction types obtained in the obtaining with two or more of second access direction types previously held in a storing unit, and judging whether or not the first and second access direction types match each other; and

executing a predetermined application program when it is judged, in the judging, that the first and second access direction types match each other.

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An integrated circuit for use with a semiconductor memory 13. card which performs contactless communication with a reader/writer, the integrated circuit comprising:

a direction obtainment unit operable to obtain a first access direction type in which the semiconductor memory card accesses the reader/writer;

a condition management unit operable to previously hold and manage an access condition including two or more of second access direction types;

a condition judgment unit operable to compare two or more of 10 the first access direction types obtained by the direction obtainment unit with two or more of the second access direction types previously held in the condition management unit, and judging whether or not the first and second access direction types match each other; and

an execution unit operable to execute a predetermined application program when the condition judgment unit judges that the first and second access direction types match each other.